



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE Docket No.: TRAN-P193 I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the below date of deposit. Date of 07/21/05 Name of Person Signature of the Person Mina Oliveri Deposit: Making the Deposit: Making the Deposit: Inventor(s): David Kuei Serial No .: 10/769,140 Group Art Unit: 2818 01/29/04 Filed: Examiner: Ngo, Ngan V. Confirmation No: 8276 Title: FRACTIONAL BIASING OF SEMICONDUCTORS Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 Sir: TRANSMITTAL OF FORMAL DRAWINGS In response to Drawing Informalities attached please find: the formal drawings for this application Number of Sheets Each sheet of drawing indicates the identifying indicia suggested in § 1.84(c) on the reverse side of the drawing a copy of the NOTICE OF INFORMAL DRAWINGS Please direct all correspondence concerning the above-identified application to the following address: **WAGNER, MURABITO & HAO LLP** Two North Market Street, Third Floor San Jose, California 95113 (408) 938-9060

Respectfully submitted,

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